

Notice of Allowability	Application No.	Applicant(s)	
	10/797,073	HAM ET AL.	
	Examiner A. Sefer	Art Unit 2826	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to amendment filed 12/28/05.

2. The allowed claim(s) is/are 1-20.

3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). ~~TECHNOLOGY CENTER 2800~~

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

NATHAN J. FLYNN
SUPERVISORY PATENT EXAMINER



* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 1) hereto or 2) to Paper No./Mail Date _____.

(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
 Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
 of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
 Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 12/28/2005 has been entered.

Allowable Subject Matter

2. Claims 1-20 are allowed.

3. The following is an examiner's statement of reasons for allowance: A reference which is pertinent to applicant's disclosure -- Lee et al. ("Lee") US PG-Pub 2004/0251460 -- teaches a carrier module body for seating a semiconductor device on an underside thereof, having a pass through hole from an upper part to the underside the semiconductor device is seated thereon; a housing over the carrier module body; a supplementary housing fitted in a lower part of the housing to be movable in up/down directions, for elastic contact with the carrier module body by a first elastic member fitted inside of the housing; a vacuum tube in the supplementary housing so as to be in communication with the pass through hole in the carrier module body; at least one pair of latches in a lower part of the carrier module body to move apart or close in an outer or inner side, for holding or releasing the semiconductor device seated on the carrier module body; at least one latch button fitted in an upper part of the carrier module body so as to be movable in up/down directions, and coupled to the at least one pair of latches with a connection pin for moving in up/down directions by an external force, to move the at least one pair of

latches; and a second elastic member for elastic supporting of the latch buttons on the carrier module body, thereby, when the semiconductor device is brought into contact with the test socket, and tested, the semiconductor device is held with a vacuum tube formed through the pass through hole in the carrier module body and the vacuum tube while the at least one pair of latches latch releases the semiconductor device. However, Lee fails to teach a housing and a supplemental housing not being constituent parts of the carrier module body. The above limitations in combination with other claim limitations are not taught or fairly suggested by the prior art nor would it be obvious to modify the references of record so as to yield carrier module body for seating a semiconductor device of the instant application.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (571) 272-1921.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ANS
February 5, 2006